

Fig.2

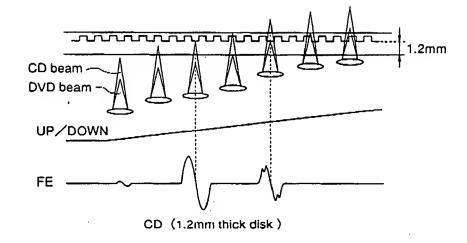
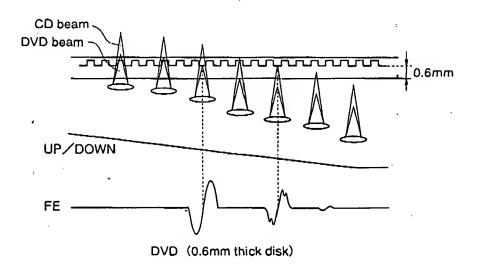
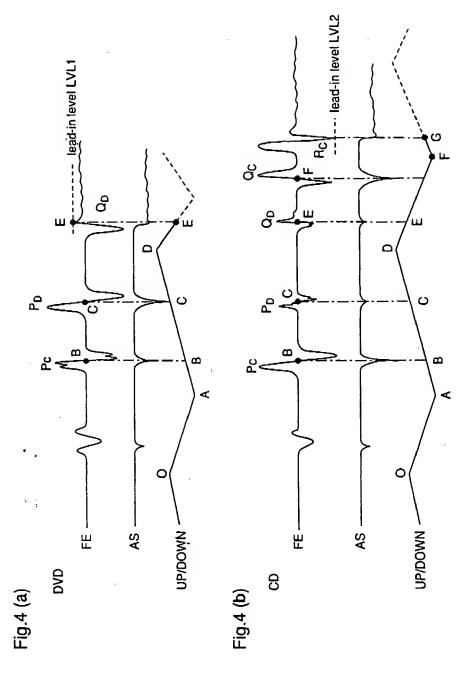


Fig.3 (b)







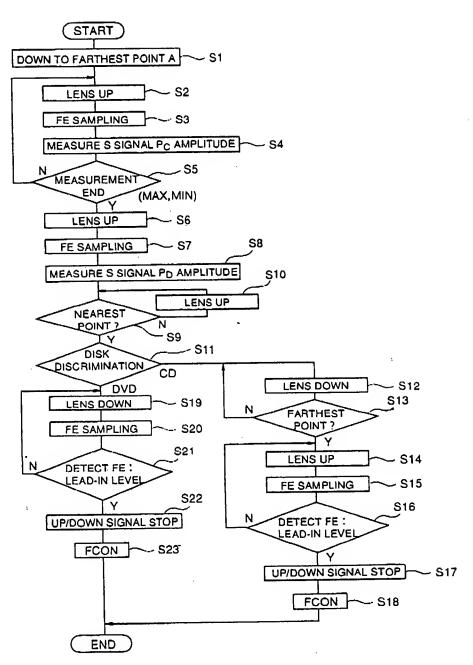


Fig.6 (a)

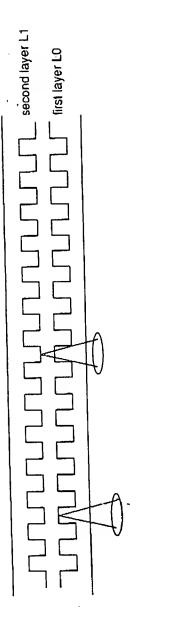
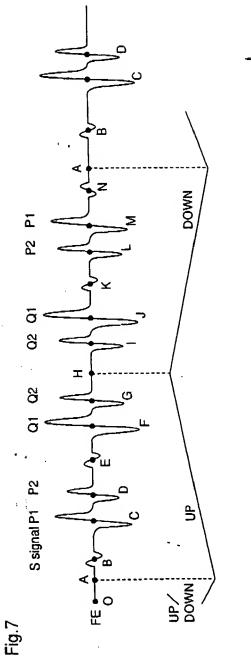
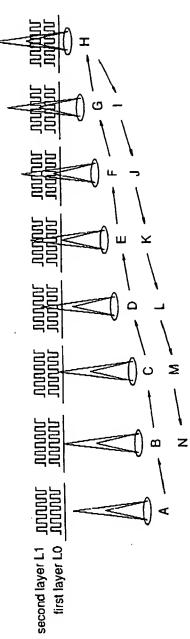


Fig.6 (b)

UNDONONDONONDONONDONON fourth layer L3 UNDONONDONONDONONDONON third layer L2 MOUNTONONDONONDONONDONON second layer L1 MOONDONONDONONDONONON second layer L1 nonny





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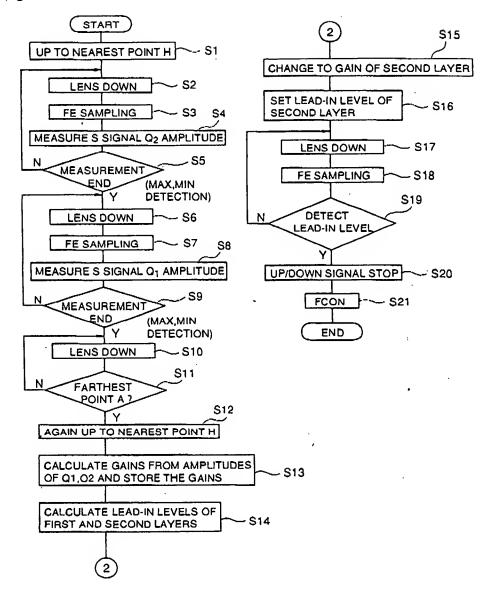
Fig.8

UP/ DOWN

품

- lead-in level

Fig.9



88/ 308 HOLD 307 305 JMP pulse 88 44 composition 98 decentration memory 302 129 DSP ភ

driving aircuit

Fig.10

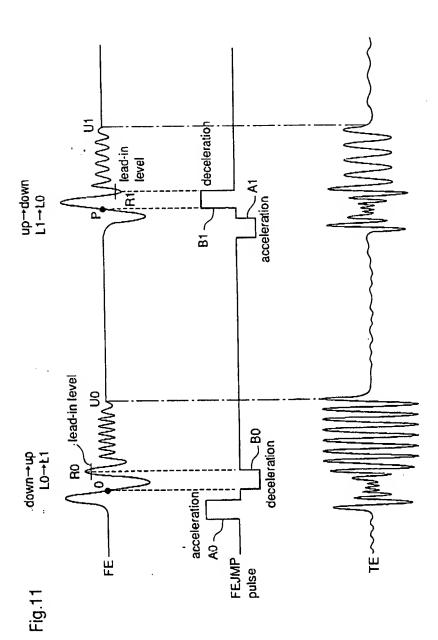
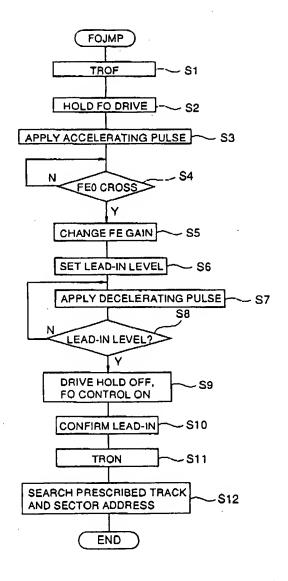
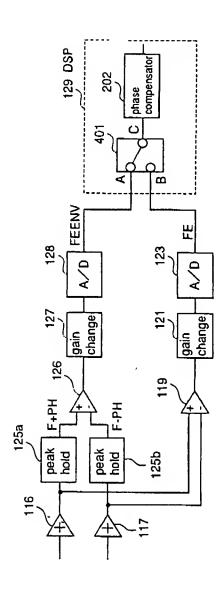


Fig.12





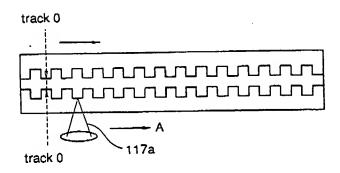


Fig.15

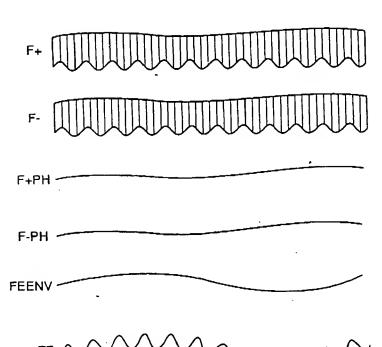


Fig.16

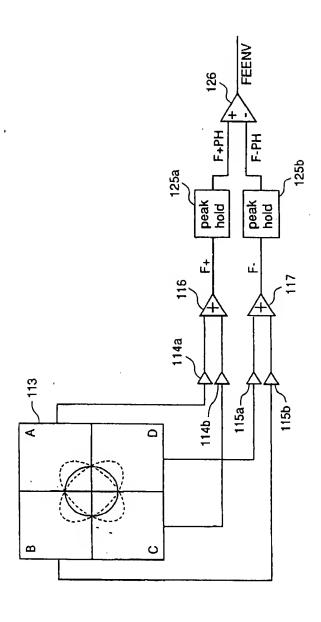
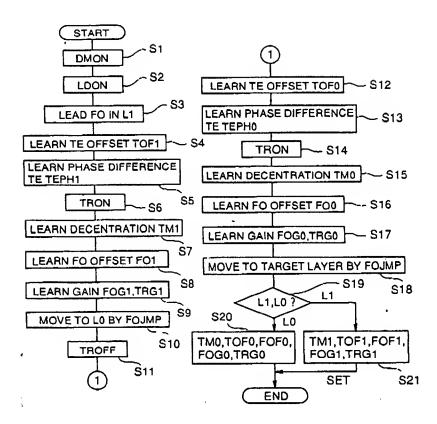
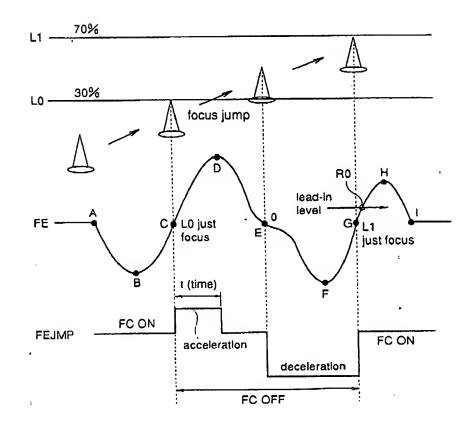
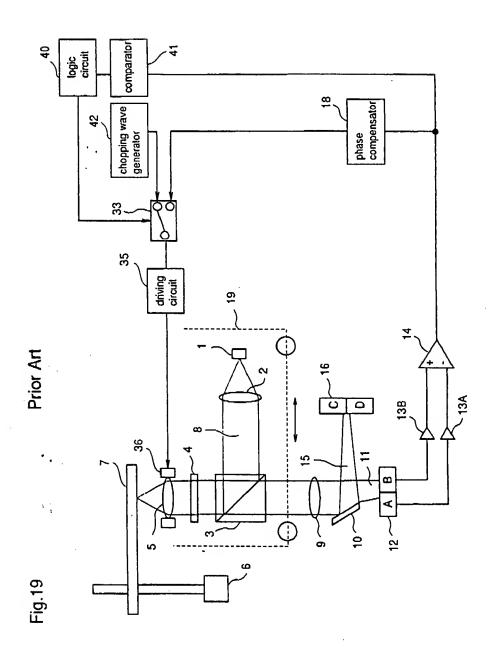


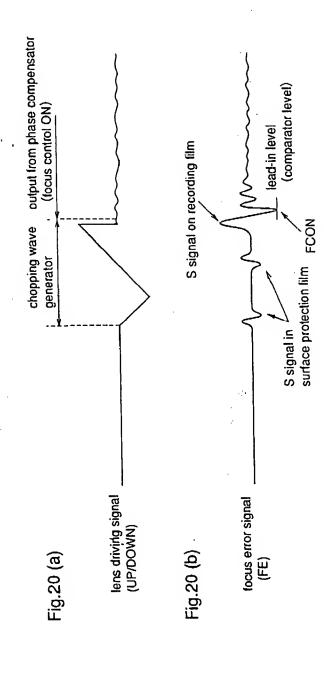
Fig.17





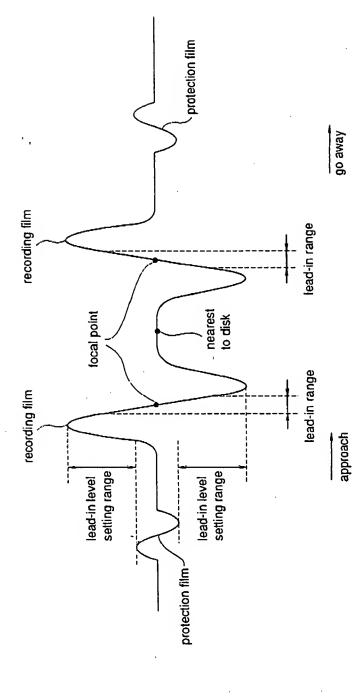


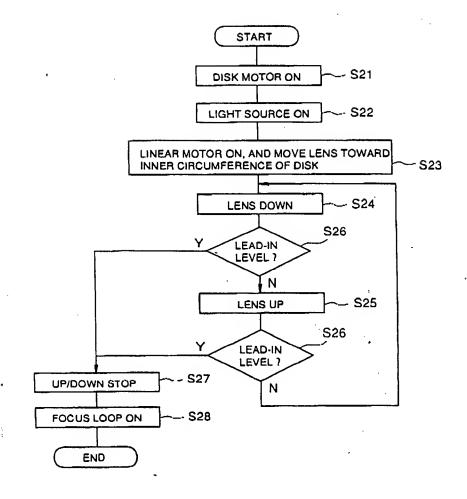












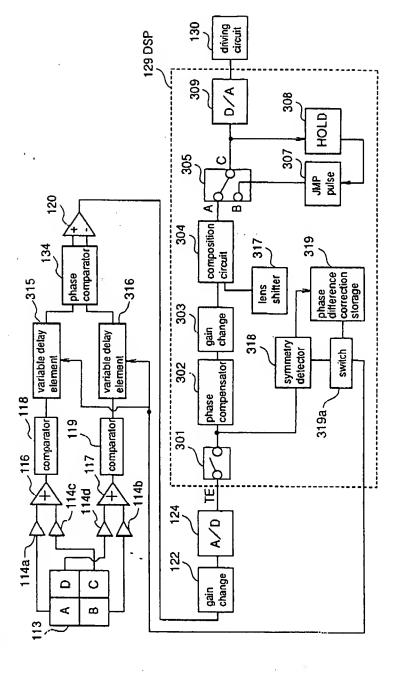
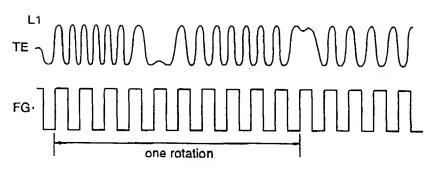
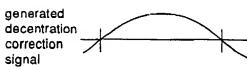
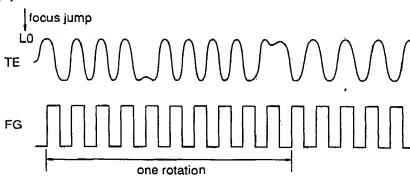


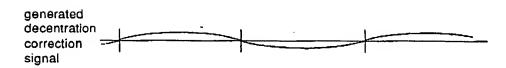
Fig.24

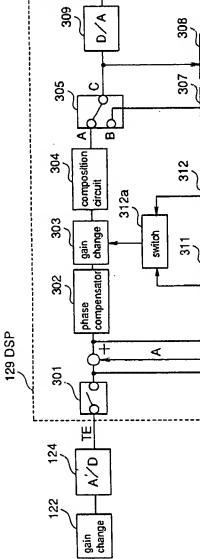












driving sircuit

HOLD

JMP pulse

gain storage

gain measure

Fig.26

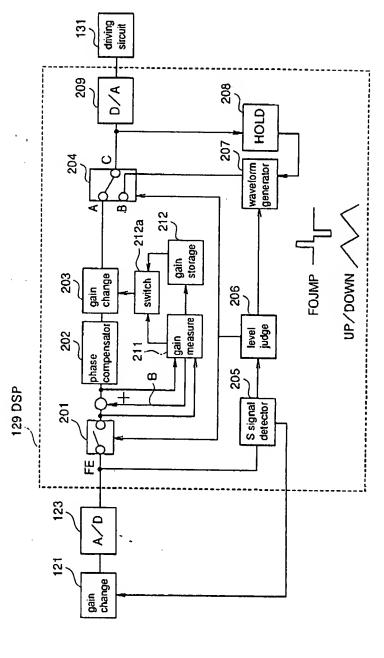


Fig.27

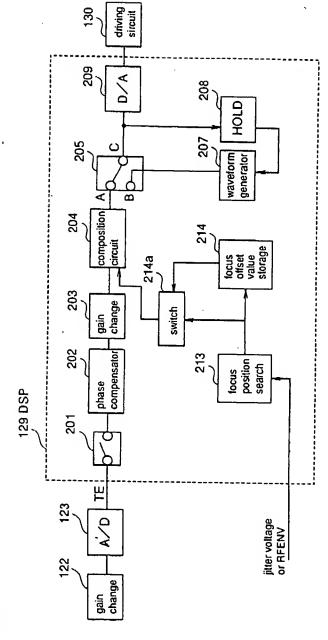


Fig.28

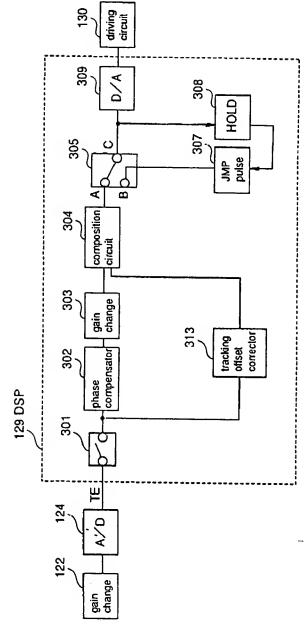


Fig.29